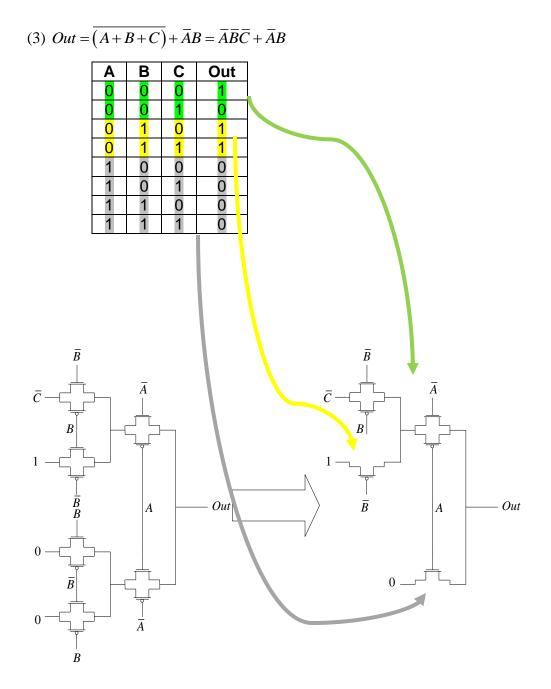
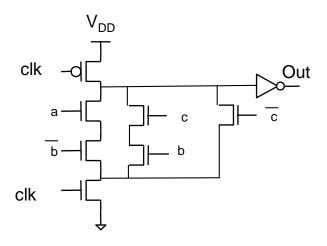
## Solutions to HW1

- (1) Flip-flop. PHI 1 and PHI2 are the two non-overlapping clocks
- (2) Flip-flop. In principle it is same as the circuit of problem 1.



(4)  $Out = A\overline{B} + BC + \overline{C}$ 



(5) CLKG will be a glitched clock.

