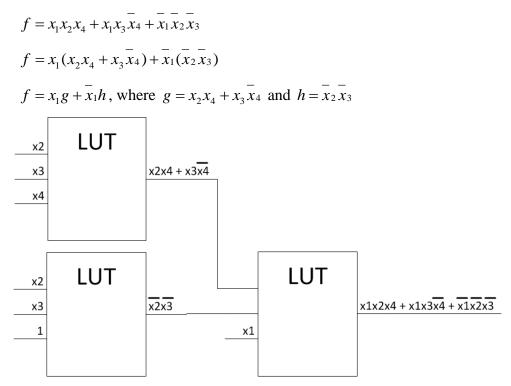
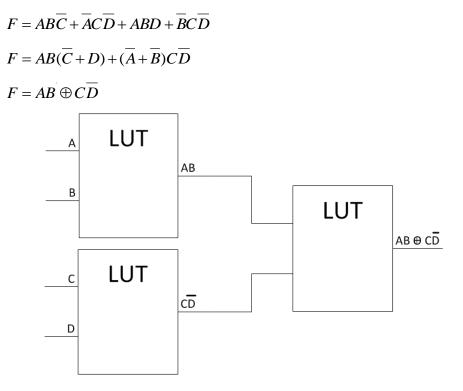
## Solutions to HW3

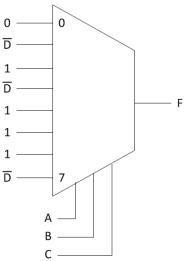
(1) Note that:

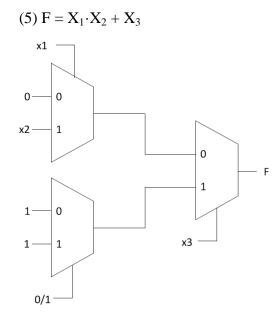


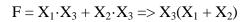
(2) Note that:

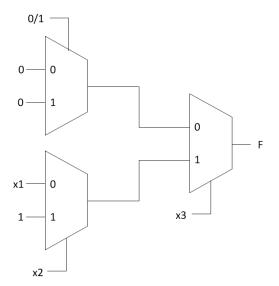


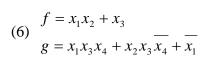
- (3) Hard Macro: Specifies how logic elements are **physically** interconnected and placed.Soft Macro: Only **logical** interconnection is specified.
- (4)  $F = A\overline{B} + B\overline{C} + C\overline{D}$

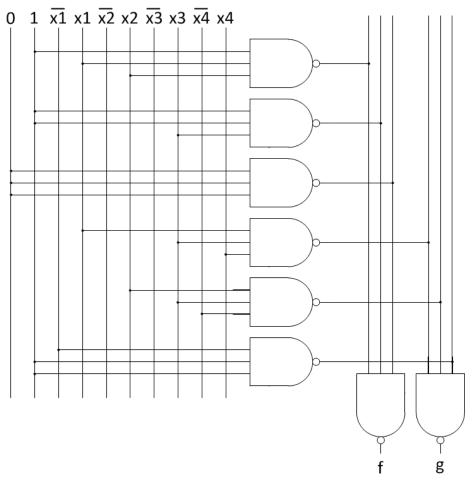


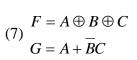


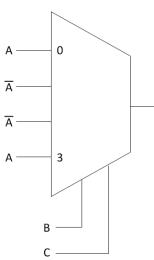












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