Homework Assignment 2

(Due 2:10pm, Sep. 27, email to daehyun.kim@wsu.edu or submit a hardcopy)

You can use the following instruction only for this homework.

- Instructions
  - ADD R$, R%, R&
  - ADD R$, R%, #imm (#imm is a constant)
  - AND R$, R%, R& // logical AND
  - AND R$, R%, #imm (#imm is a constant)
  - ORR R$, R%, R& // logical OR
  - ORR R$, R%, #imm (#imm is a constant)
  - EOR R$, R%, R& // logical XOR
  - EOR R$, R%, #imm (#imm is a constant)

1. (20 points) Generate the following output signal Y from the input signal A using the instruction above. You can assume that A and Y are 4-bit registers.

- $A = a_3a_2a_1a_0$ (stored in R0)
- $Y = 01a_1\overline{a_0}$ (store Y in R2)

2. (30 points) Generate the following output signal Y from the input signals A and B using the instruction above. You can assume that A, B, and Y are 4-bit registers.

- $A = a_3a_2a_1a_0$ (stored in R0)
- $B = b_3b_2b_1b_0$ (stored in R1)
- $Y = \{a_3\&b_3\}\{a_2|b_2\}\{a_1\}\{b_0\}$ (store Y in R2). In other words, if $Y = y_3y_2y_1y_0$, then
  - $y_3 = a_3$ AND b_3
  - $y_2 = y_2$ OR b_2
  - $y_1 = a_1$
  - $y_0 = b_0$

3. (50 points) Generate the following output signal Y from the input signals A and B using the instruction above. You can assume that $A, B,$ and Y are 4-bit registers.

- $A = a_3 a_2 a_1 a_0$ (stored in R0)
- $B = b_3 b_2 b_1 b_0$ (stored in R1)
- $Y = A + 2$ if B is an even number and $A + 1$ if B is an odd number. (store Y in R2)
    - (You don't need to worry about overflows.)