Suppose R\# is an 8-bit register. The data stored in R\# is treated as an unsigned binary number. Explain what "arithmetic" operation the following instruction performs.
INV R2, R1
(Notice that "INV" performs bitwise inversion for input R1 and stores the result to R2.)

$$
R_{2}=\overline{R_{1}}
$$

The following shows all the bits of $R_{2}$.

$$
\begin{aligned}
& R_{1}=x_{7} x_{6} \ldots x_{0} \\
& R_{2}=\overline{x_{7} x_{6}} \ldots \overline{x_{0}}
\end{aligned}
$$

If you add the two, you get

$$
R_{1}+R_{2}=11 \ldots 1=255
$$

Thus, the "arithmetic" operation it performs is

$$
R_{2}=255-R_{1}
$$

Suppose R\# is an 8-bit register. The data stored in R\# is treated as a two's complement binary number. Explain what "arithmetic" operation the following instruction performs for input R1.

## AND R2, R1, 0x7F

If R1 is zero or positive, R2 = R1.
If R1 is negative,

$$
\begin{gathered}
R_{1}=1 x_{6} x_{5} \ldots x_{0} \\
R_{2}=0 x_{6} x_{5} \ldots x_{0} \\
\left|R_{1}\right|=0 \overline{x_{6} x_{5}} \ldots \overline{x_{0}}+1 \\
R_{2}+\left|R_{1}\right|=01111111_{2}+1=128 \\
R_{2}=128-\left|R_{1}\right|
\end{gathered}
$$

It performs $128-\left|R_{1}\right|$ (for a negative number) just by one logical instruction.

Suppose R\# is an 8-bit register. The data stored in R\# is treated as an unsigned binary number. Draw a graph for the following instruction. The x-axis should be the value stored in R1 and the y-axis should be the value stored in R2.

> EOR R2, R1, 0x01
(Notice that "EOR" performs bitwise exclusive-OR between input R1 and 0x01 and stores the result to R2.)

$$
\begin{aligned}
& R_{1}=x_{7} x_{6} \ldots x_{0} \\
& R_{2}=x_{7} x_{6} \ldots \overline{x_{0}}
\end{aligned}
$$

If $R_{1}$ is even $\left(x_{0}=0\right), R_{2}=R_{1}+1(y=x+1)$.
If $R_{1}$ is odd $\left(x_{0}=1\right), R_{2}=R_{1}-1(y=x-1)$.
Well, you might express it as

$$
R_{2}=R_{1}+(-1)^{R_{1}}
$$

but the goal is to draw the graph, so you don't need to express it like that.


Suppose R\# is an 8-bit register. The data stored in R\# is treated as an unsigned binary number. Draw a graph for the following instruction. The x-axis should be the value stored in R1 and the y-axis should be the value stored in R2.

EOR R2, R1, 0x02
(Notice that "EOR" performs bitwise exclusive-OR between input R1 and 0x01 and stores the result to R2.)

$$
\begin{aligned}
& R_{1}=x_{7} x_{6} \ldots x_{1} x_{0} \\
& R_{2}=x_{7} x_{6} \ldots \overline{x_{1}} x_{0}
\end{aligned}
$$

If $x_{1}$ is 0 (then, $R_{1}$ is $4 k$ or $\left.4 k+1\right), R_{2}=R_{1}+2(y=x+2)$.
If $x_{1}$ is 1 (then, $R_{1}$ is $4 k+2$ or $4 k+3$ ), $R_{2}=R_{1}-2(y=x-2)$.
Well, you might express it as

$$
R_{2}=R_{1}+(-1)^{\left(R_{1} \operatorname{DIV} 2\right)} \cdot 2
$$

where (DIV calculates the quotient of the integer division), but the goal is to draw the graph, so you don't need to express it like that.


