

Lecture 17

Note Title

3/18/2008

Multi-threaded architectures

Many processors 64 - 128

Each processor supports 128 thread execution contexts.

- switches betw. cycle by cycle.

- it doesn't use caches

- thread switching hides the latency of main memory

Memory supports atomic add (and others)

- occupied / free for every word.

- load when full

- store when empty

Thread switching for coordination of effort betw. threads.

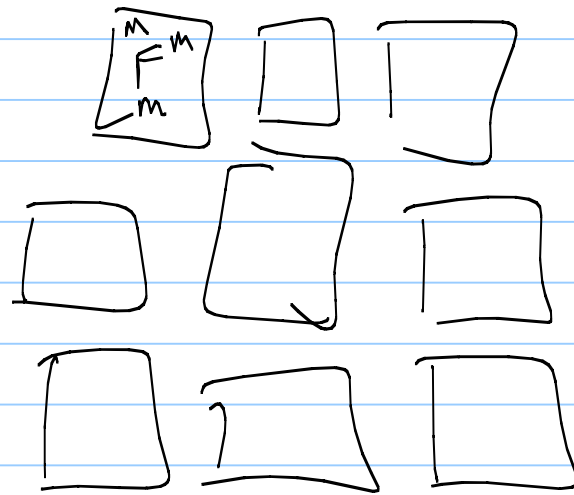
Fortran

C

Implementations -

Agent-style computing -

Simulations -



Hyenas in distant
habitats are more closely
related than ones
in adjacent habitats
Unusual in animal kingdom.