Corrected example of CRC computation from the slides today.

The example of the CRC computation on today's slides was incorrect.

Let's try again.

The message for which we want to compute the CRC is D=101110.
The Generator specified is G=1001. The checksum will have 1 fewer bits than the Generator, i.e. r=3.
To compute the checksum, R, shift D left r bits (appending 0s) then divide by G, using XOR in the role of subtraction in ordinary long division.

```
      ___101011  -  the quotient is not used
1001|101110000
     1001
      0101
      0000
       1010
       1001
        0110
        0000
         1100
         1001
          1010
          1001
           011  = R, the CRC checksum
```

The message sent is therefore 101110011. If you divide this by the generator G you should get a remainder of 000. Let's see:

```
      ___101011  -  the quotient is not used
1001|101110011
     1001
      0101
      0000
       1010
       1001
        0110
        0000
         1101
         1001
          1001
          1001
           000
```

I suggest you try going through the computation with a changed bit and see what happens.