EE334 Computer Architecture   **Spring 2004**

Exam 2:   Monday April 5, 2004

Topics (from textbook)

Chapter 4. Arithmetic for Computers

4.2. Signed and unsigned numbers
4.3. Addition and Subtraction
4.4. Logical Operations
4.5. Constructing an Arithmetic Logic Unit
4.6. Multiplication
4.8. Floating point (up to page 287)

Chapter 5. The Processor: Datapath and Control

5.1. Introduction
5.2. Building a Datapath
5.3. A Simple Implementation Scheme
5.4. A Multicycle Implementation
5.5. Microprogramming: Simplifying Control Design

Appendix C. Mapping Control to Hardware

C.2. Implementing Combinational Control Units
C.3. Implementing Finite State Machine Control
C.4. Implementing the Next-State Function with a Sequencer

Open book and notes
Bring a calculator