EE424 Digital System Architecture   **Spring 2003**

Final Exam: Wednesday May 7, 2003
7-9pm, Room: Sloan 46

Topics (from textbook)

Chapter 6. Enhancing performance with Pipelining

6.1   An Overview of Pipelining
        -Pipelining Hazards: Structural, Control and Data
6.2   A Pipelined Datapath
6.4   Data Hazards and Forwarding
6.5   Data hazards and Stalls
6.6   Branch Hazards (up to page 498)

Chapter 7. Memory Hierarchy

7.1   Introduction
        -Principle of Locality
7.2   The Basics of Caches

Open book and notes
Bring a calculator