**EE334 Computer Architecture Homework Assignment 5**

Control Finite State Machine Due: Wednesday, March 10, 2010

The finite state machine (FSM) that forms the control part of the processor does not implement the instruction add immediate (*addi*). Please modify the FSM (shown below) to include the instruction addi. Please assume that (Op = ‘addi’) is included. You may need to add more states to the FSM; please try to use the states already in the FSM, if possible.

